## 15750 U.S. PTO



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Date: March 10, 2004



## **Certificate of Express Mailing**

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	Date: March 10, 2004
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	Marc A. Rossi
P.O. B	MISSIONER FOR PATENTS Sox 1450 adria, VA 22313-1450
Sir:	
Transr	nitted herewith for filing is the patent application of
Invent	or(s): Shoichi FURUHATA of Nagano, JAPAN
For:	SEMICONDUCTOR WAFER, SEMICONDUCTOR DEVICE, AND PROCESS FOR MANUFACTURING THE SEMICONDUCTOR DEVICE
Enclos	ed are:
[X]	SIX (6) sheets of formal drawings, Figures 1 - 8.
[X]	An Assignment of the invention to Fuji Electric Device Technology Co., Ltd.
[X]	The application claims priority from Japanese Application 2003-129541 filed May 7, 2003.
[X]	A certified copy of the priority document is enclosed.
[]	A Declaration and Power of Attorney.

- [ ] A verified statement to establish small entity status under 37 CFR 1.9 and 37 CFR 1.27.
- [ ] A Preliminary Amendment is enclosed.
- [X] An Information Disclosure Statement.

	Number	Number		Basic		
	Filed	Extra	Rate	Fee	\$	770.00
Total						
Claims	13 - 20	= 0	x \$ 18.00	<del>=</del> =	\$	0.00
Independent						
Claims	3 - 3	= 0	x \$ 86.00	=		0.00
Multiple Dependent Claim(s) (if applicable) + \$290.00 =						0.00
			Total		\$	770.00
Reduction by		0.00				
Assignment re	cordation f	ee	\$ 40.00	=	\$	0.00
	\$	770.00				

- [ ] The Commissioner is hereby authorized to charge the above filing fees in the amount of \$\_\_\_\_\_, and any additional fees which may be required, or credit any overpayment, to our Deposit Account No. 18-2056.
- [] The Commissioner is hereby authorized to charge payment of the following fees during the pendency of this application or credit any overpayment to our Deposit Account No. 18-2056: any patent application processing fees under 37 CFR 1.17; and any filing fees under 37 CFR 1.16 for presentation of extra claims.

Marc A. Rossi

Registration No. 31,923